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Vlsi Analog Signal Processing Circuits

Hongjiang Song

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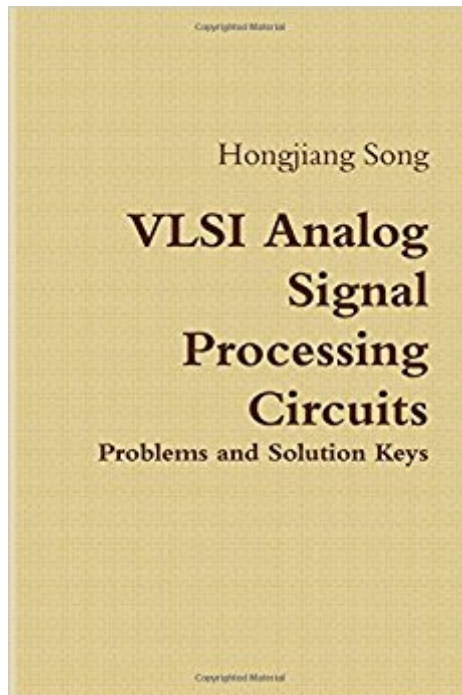
Synopsis

This book is based on a collection of the past exams for the Vlsi Analog Signal Processing Circuits class (Eee598) the author offered in the School of Engineering at Arizona State University. The topics cover various aspects of the design, analysis and application of Vlsi analog signal processing circuits. This book is intended to be used together with the Vlsi Analog Signal Processing Circuits textbook by the same author. It can also be used alone for the experienced readers.

Book Information

Paperback: 608 pages

Publisher: lulu.com (February 14, 2014)

Language: English

ISBN-10: 1304749495

ISBN-13: 978-1304749499

Product Dimensions: 6 x 1.5 x 9 inches

Shipping Weight: 2.2 pounds (View shipping rates and policies)

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Customer Reviews

Dr. Hongjiang Song (Temple, AZ) is a lecturer with the School of Electrical, Computer, and Energy Engineering at Arizona State University, where he has taught a series of circuit design courses in VLSI Analog Signal Processing, VLSI High-Speed I/O Circuits, and Structural VLSI Analog Circuits since 2002. Dr. Song earned his PhD from ASU and has been working in the VLSI circuit industry as a senior design engineer with Intel Corporation since 1994. --This text refers to an out of print or unavailable edition of this title.

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